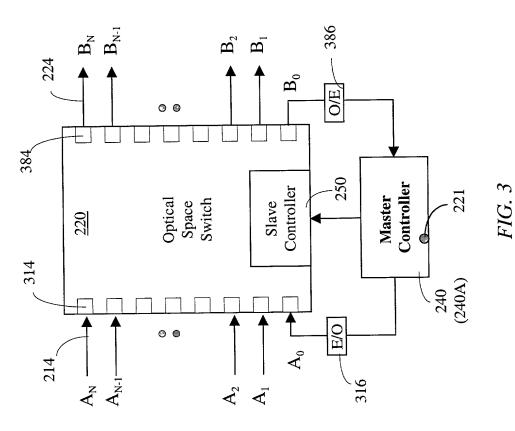
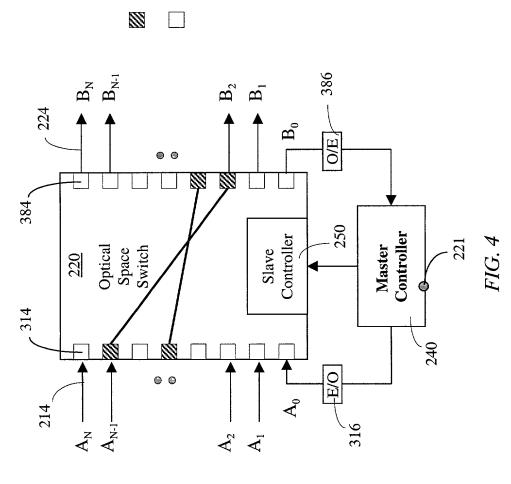


FIG. 2





Channel switched

Burst switches

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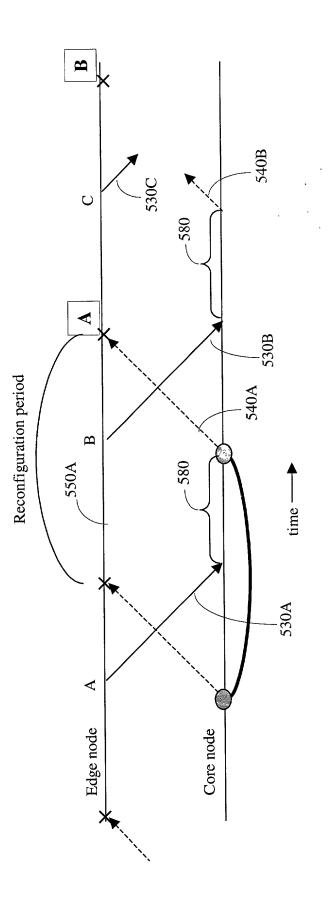


FIG (

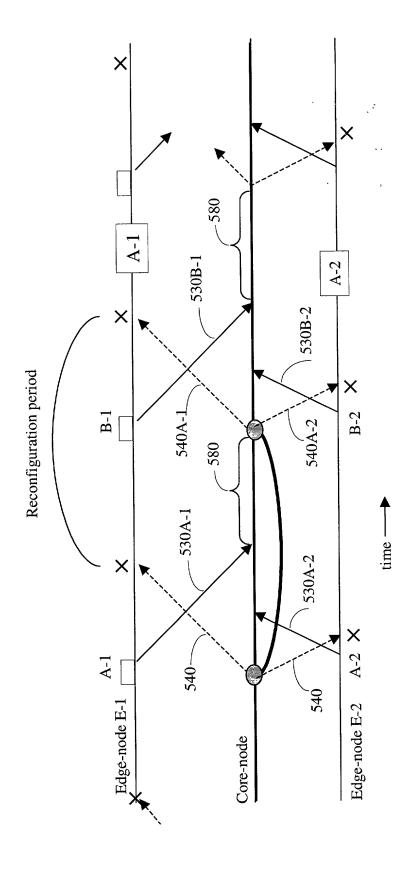
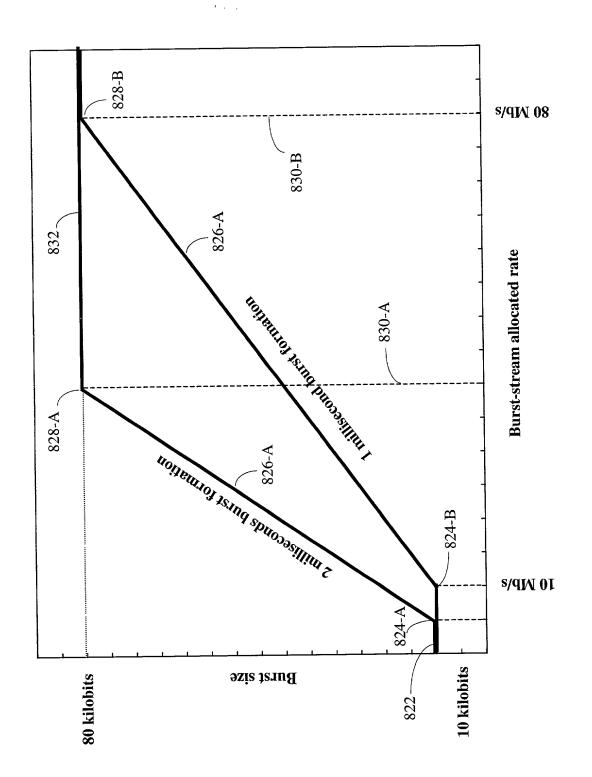
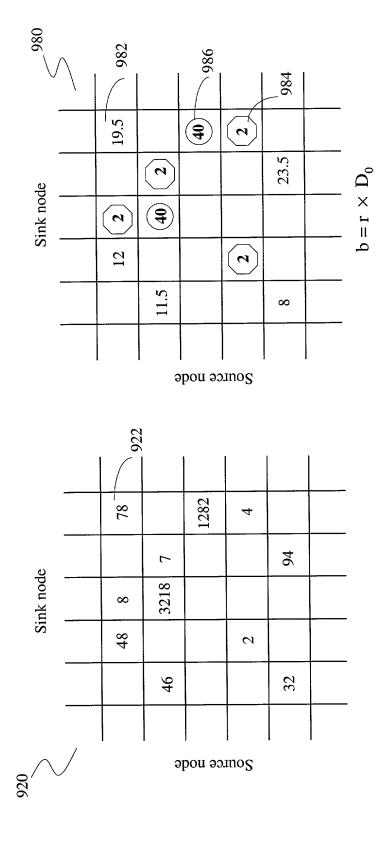
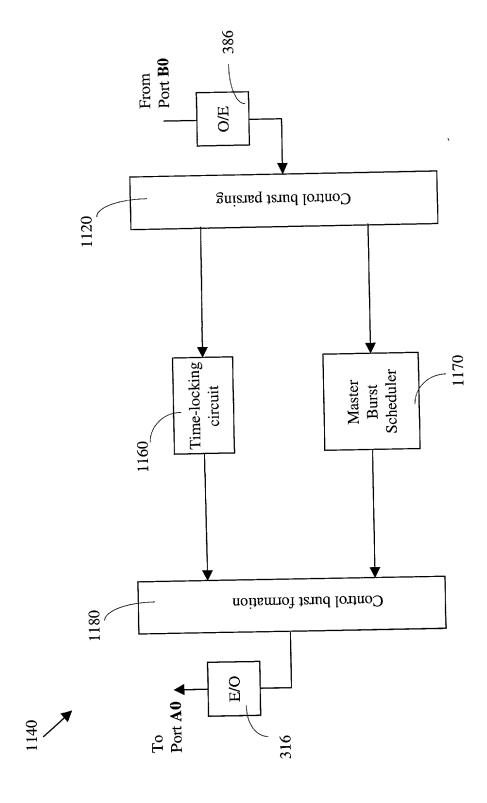


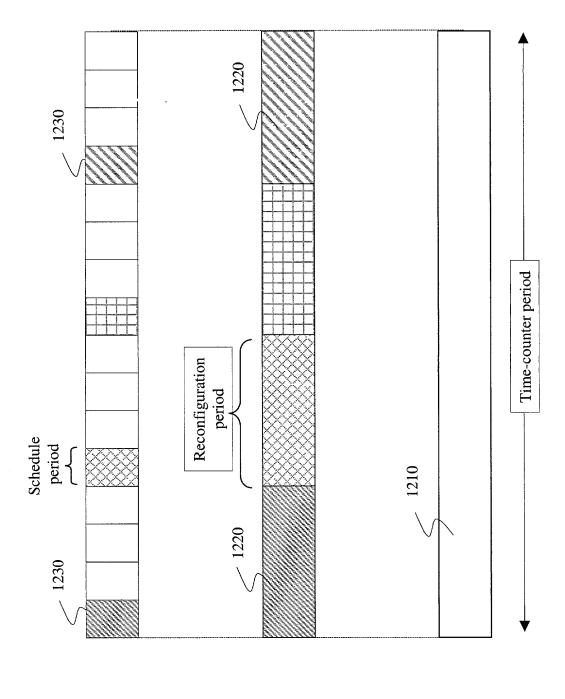
FIG. 7



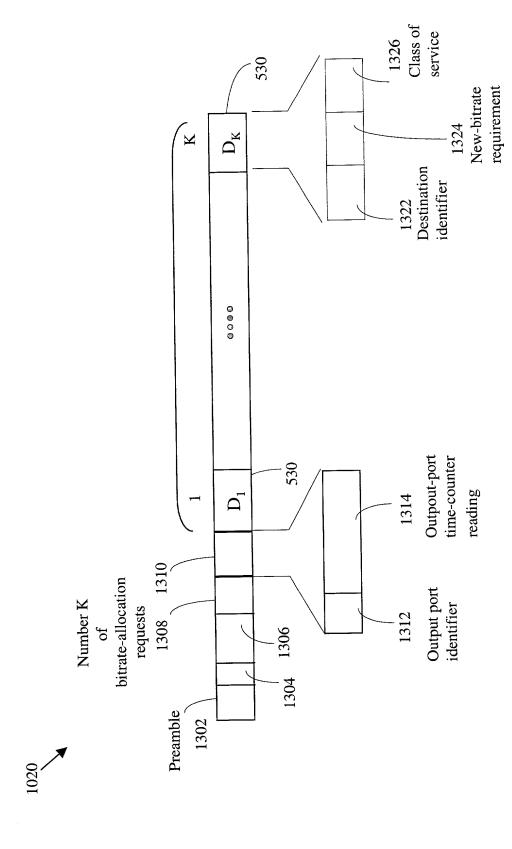


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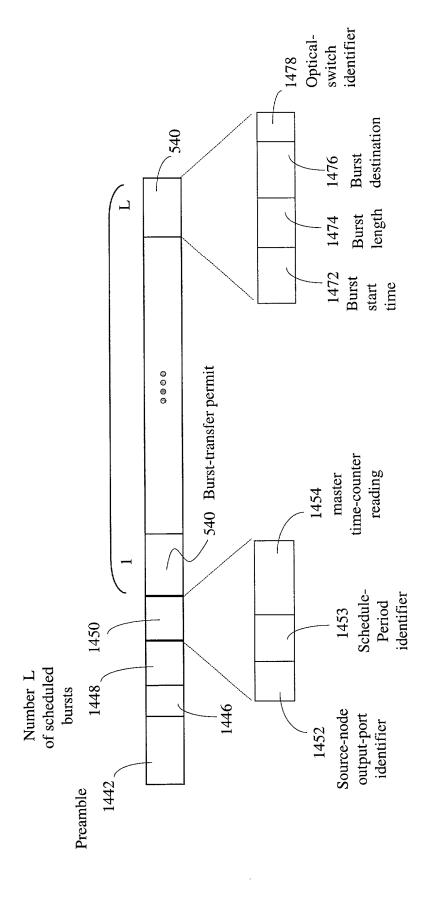
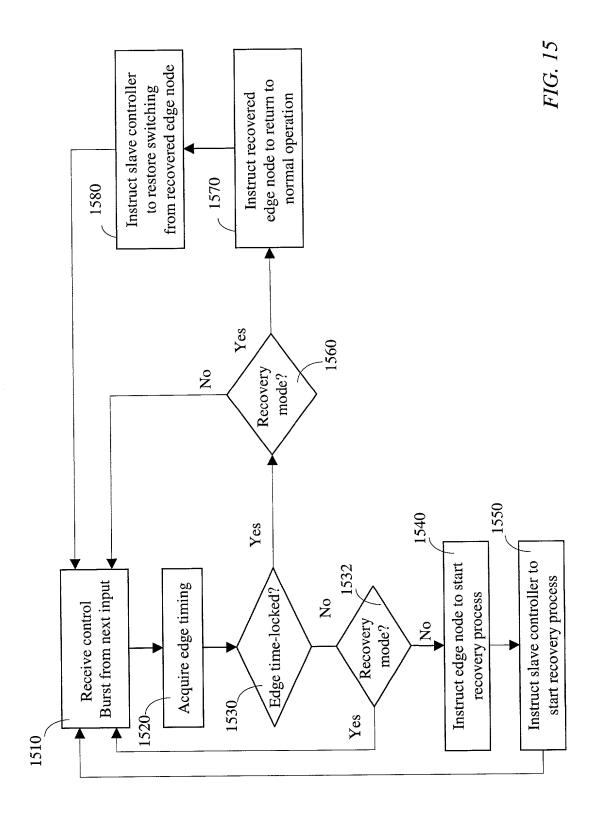
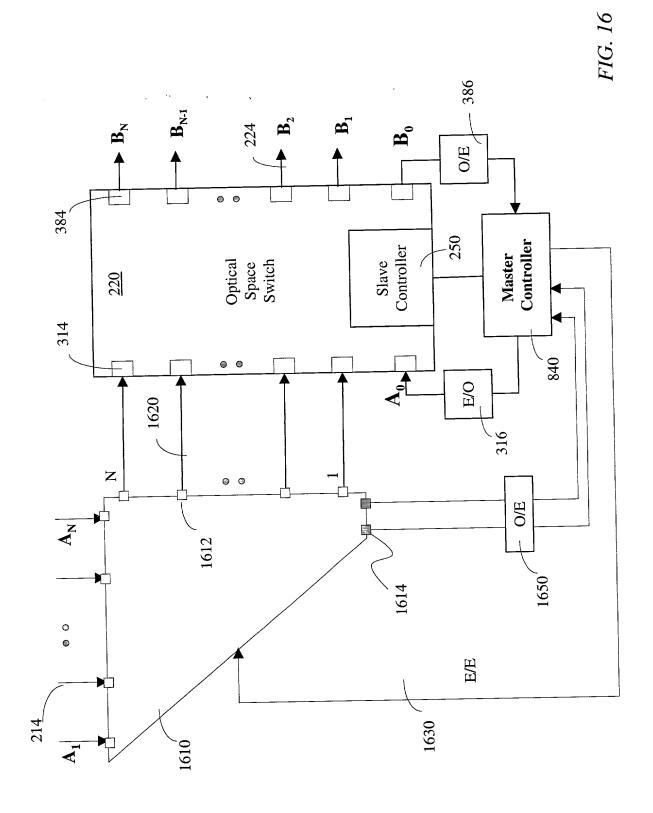
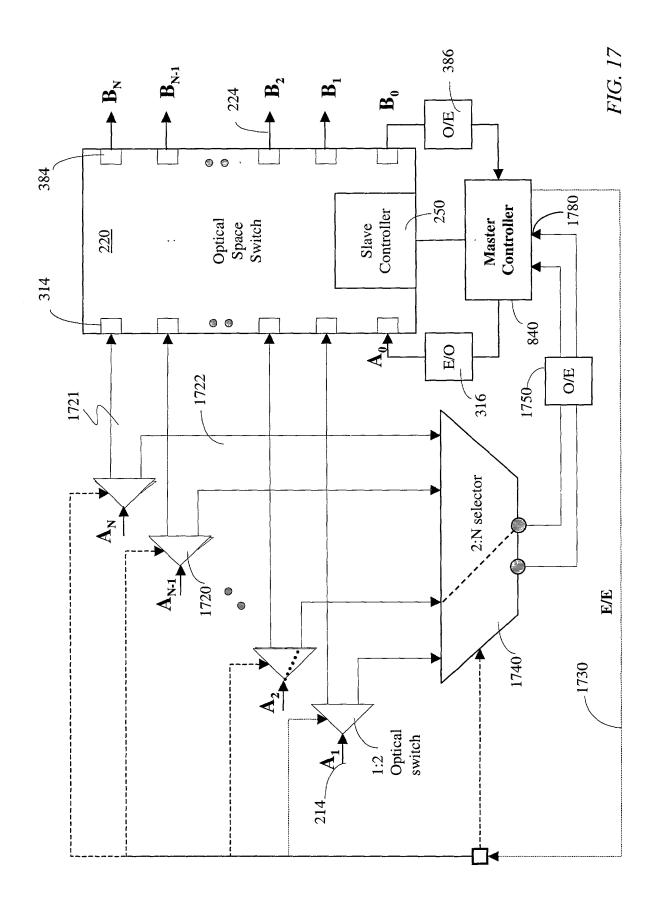
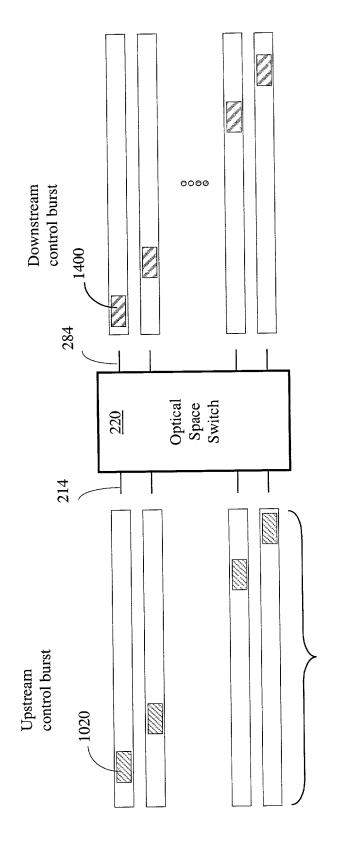


FIG. 14



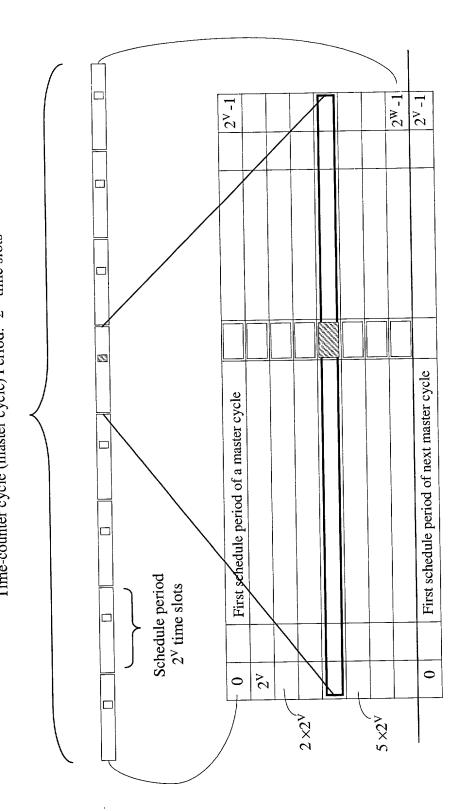






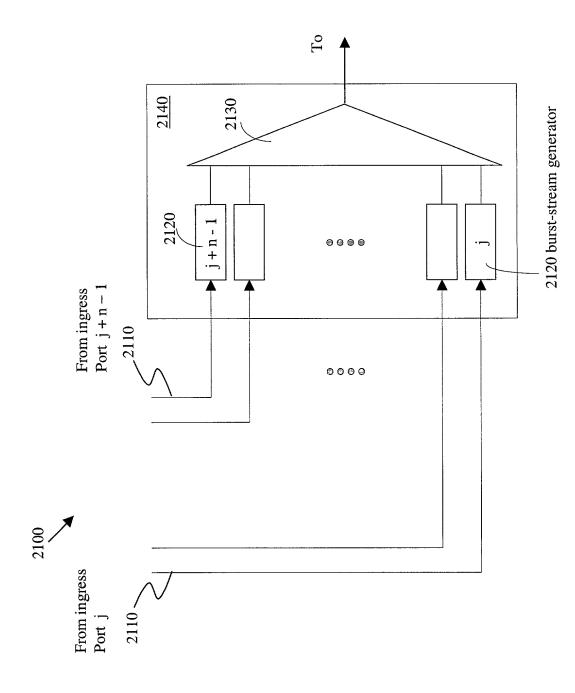
Reconfiguration period

THUE IF . I TITI

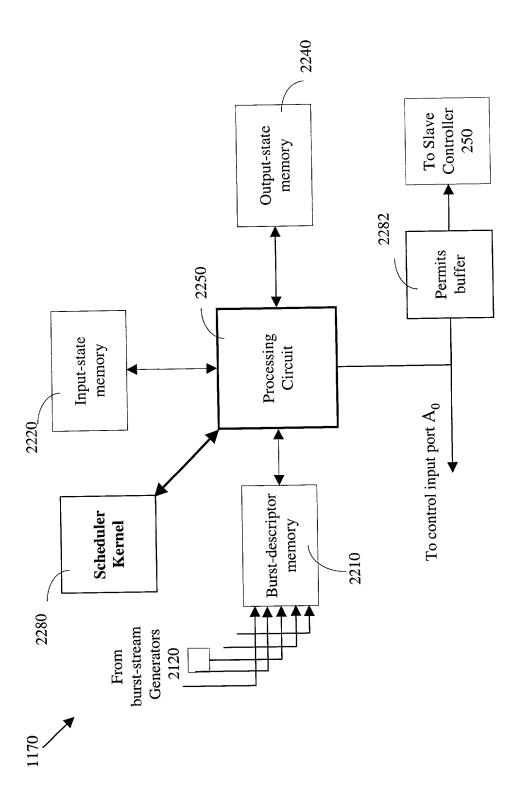


140

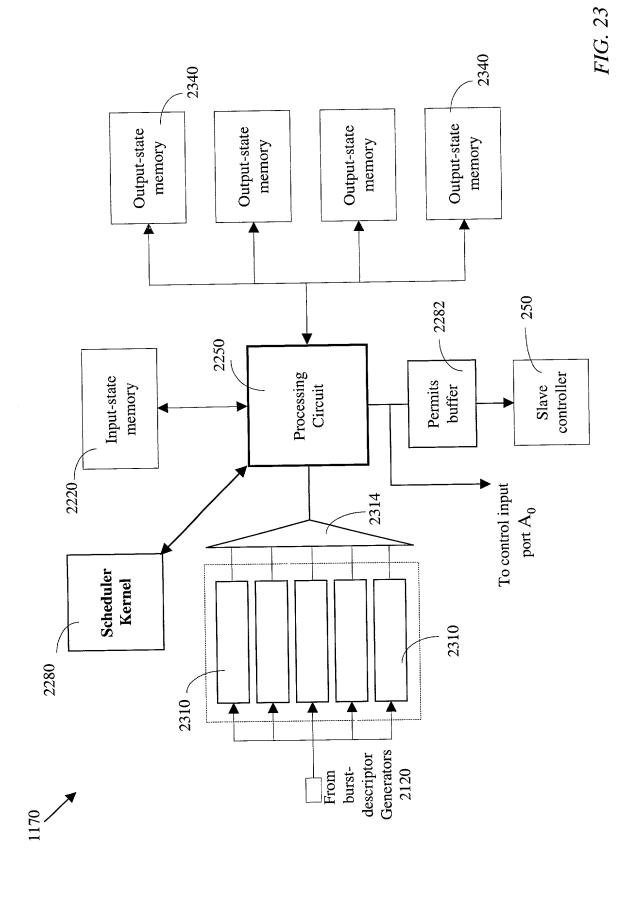
THUNGSIN . I I I JULI

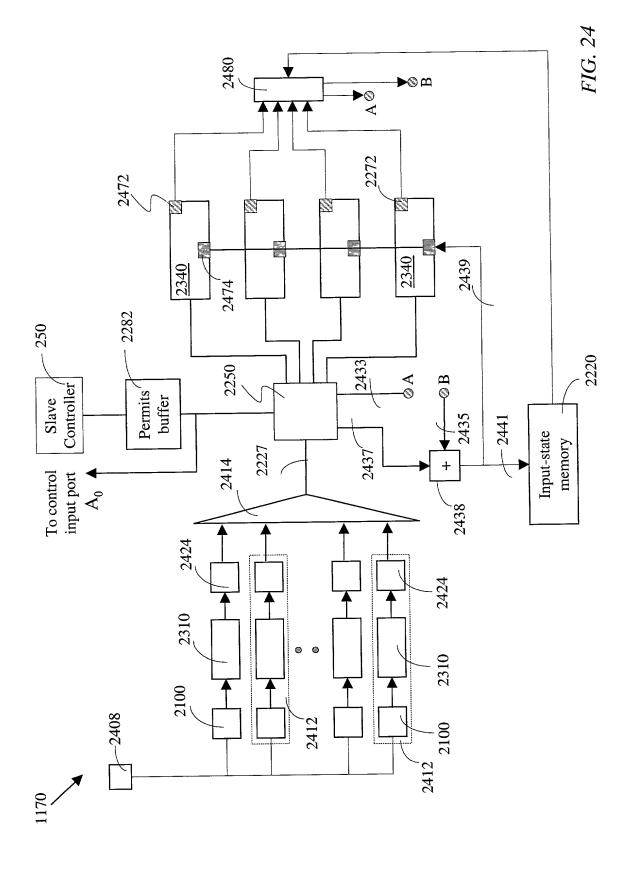


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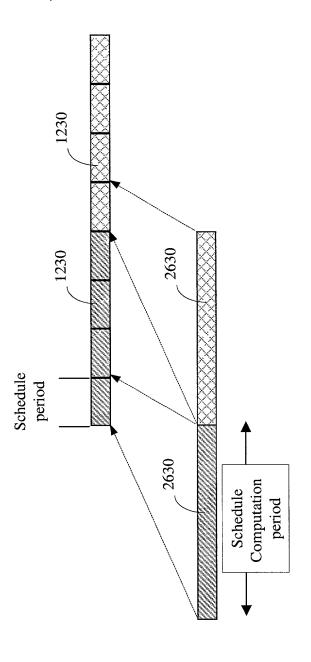
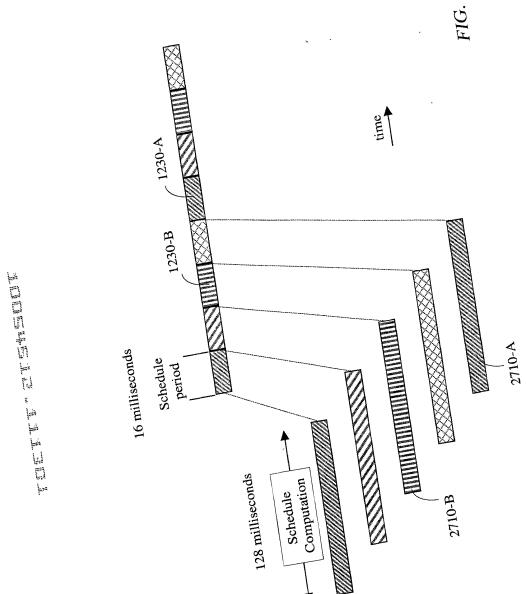


FIG. 26



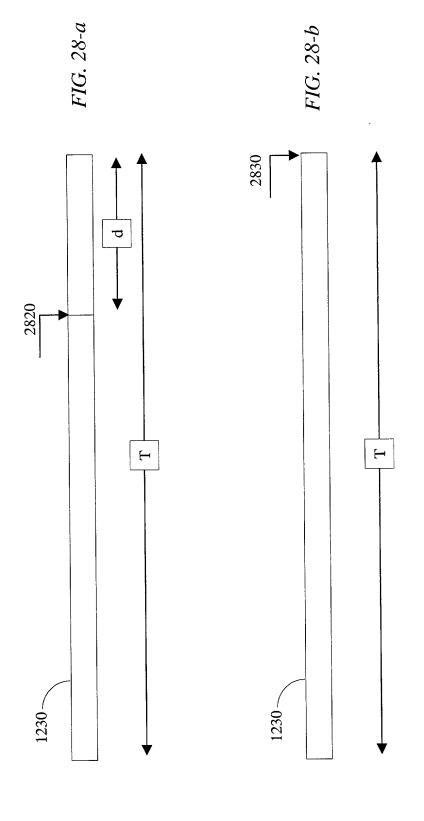
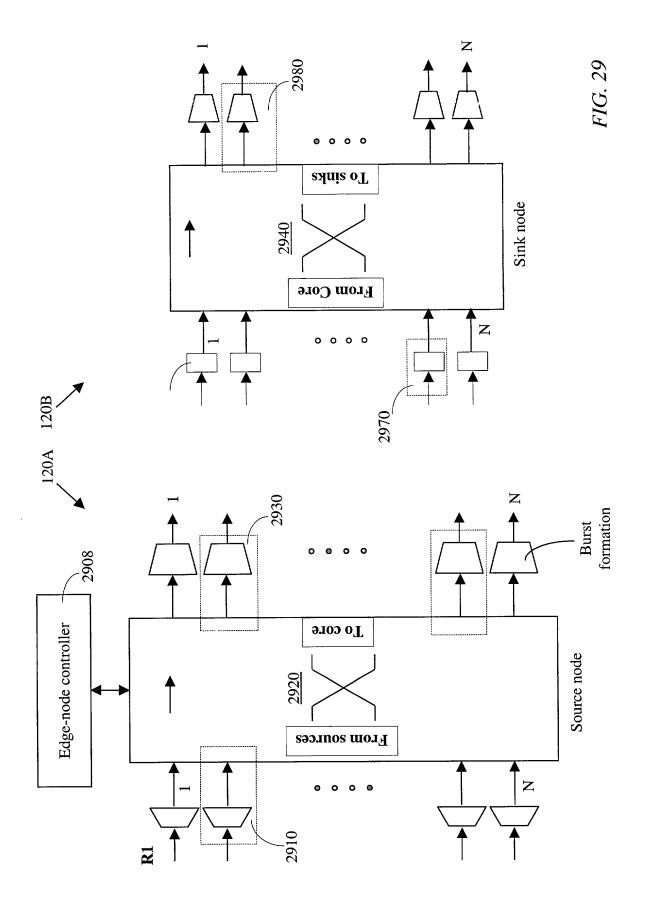


FIG. 28

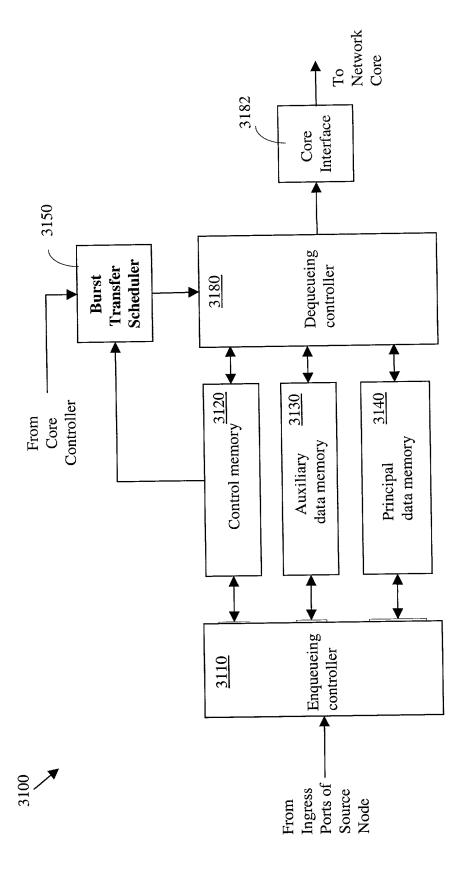


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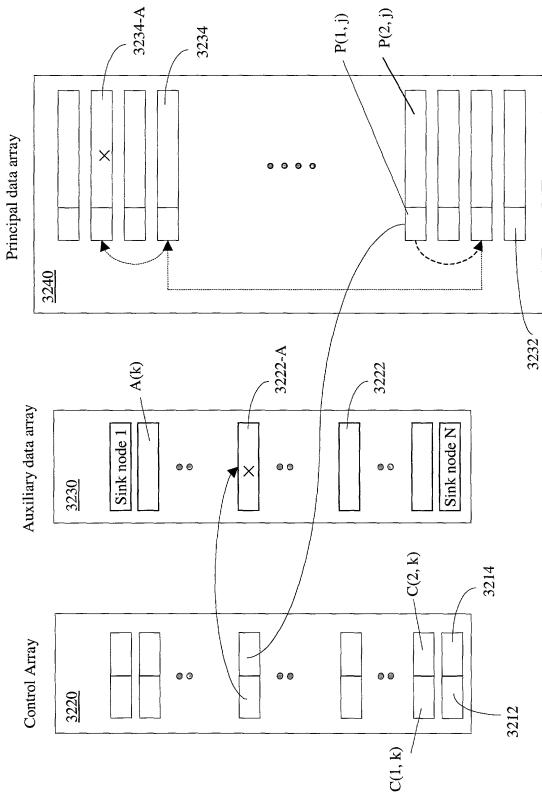
To core

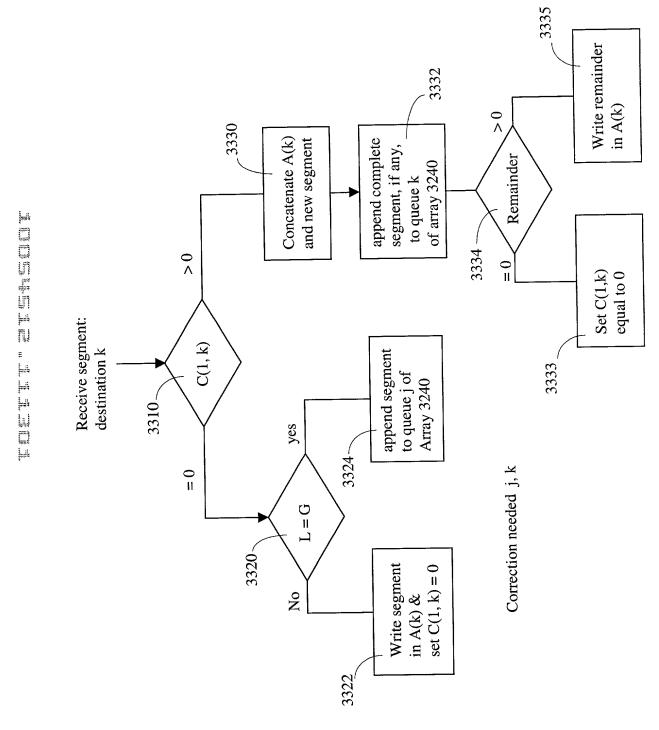
FIG. 30

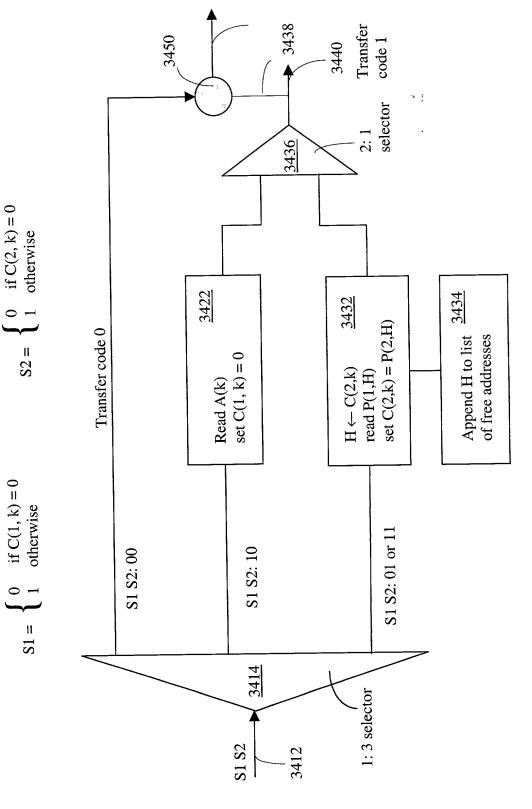
To sinks



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